

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6184	(707/1).CCLS.	US-PGPUB; USPAT; EPO	OR	OFF	2007/12/28 17:28
L2	2274	(713/2).CCLS.	US-PGPUB; USPAT; EPO	OR	OFF	2007/12/28 17:28
L3	2390	(365/189.09).CCLS.	US-PGPUB; USPAT; EPO	OR	OFF	2007/12/28 17:28
L4	0	1 and 2 and 3	US-PGPUB; USPAT; EPO	OR	ON	2007/12/28 17:28
L5	10843	1 2 3	US-PGPUB; USPAT; EPO	OR	ON	2007/12/28 18:06
L6	0	2 and 3	US-PGPUB; USPAT; EPO	OR	ON	2007/12/28 17:36
L7	24	5 and bias\$4 with circuit with ground and (program application logic adj state state) with (SRAM static adj random adj access adj memory)	US-PGPUB; USPAT; EPO	OR	ON	2007/12/28 18:07